

PRIOR ART

FIG. 1

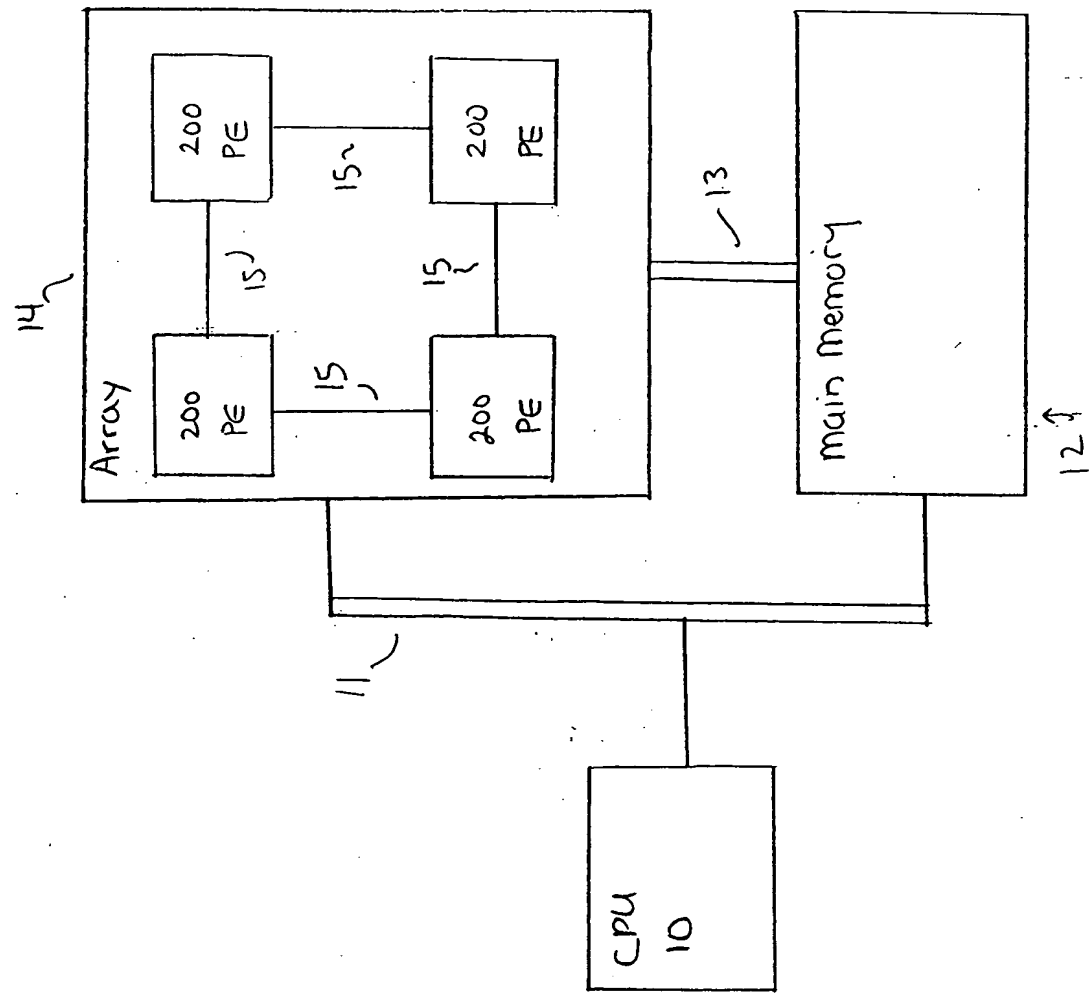


FIG. 2

FIG 30

Q-Block 350b

M-Block 350a

Q-Bus 307b

M-Bus 307a

307a

309a

308a

Q3

330

Q2

331

Q1

332

Q0

333

QS

334

309b

308b

G

320

M3

310

M2

311

M1

312

M0

313

MS

314

REGISTER
FILE

302

ALU

301

Node
Communications
Interface

305

307

FLAGS

SHIFT CONTROL
REGISTER

308

DRAM
Interface

303

304

306a

306b

306c

306d

FIG 3

FIG 4A

SCR 1

400

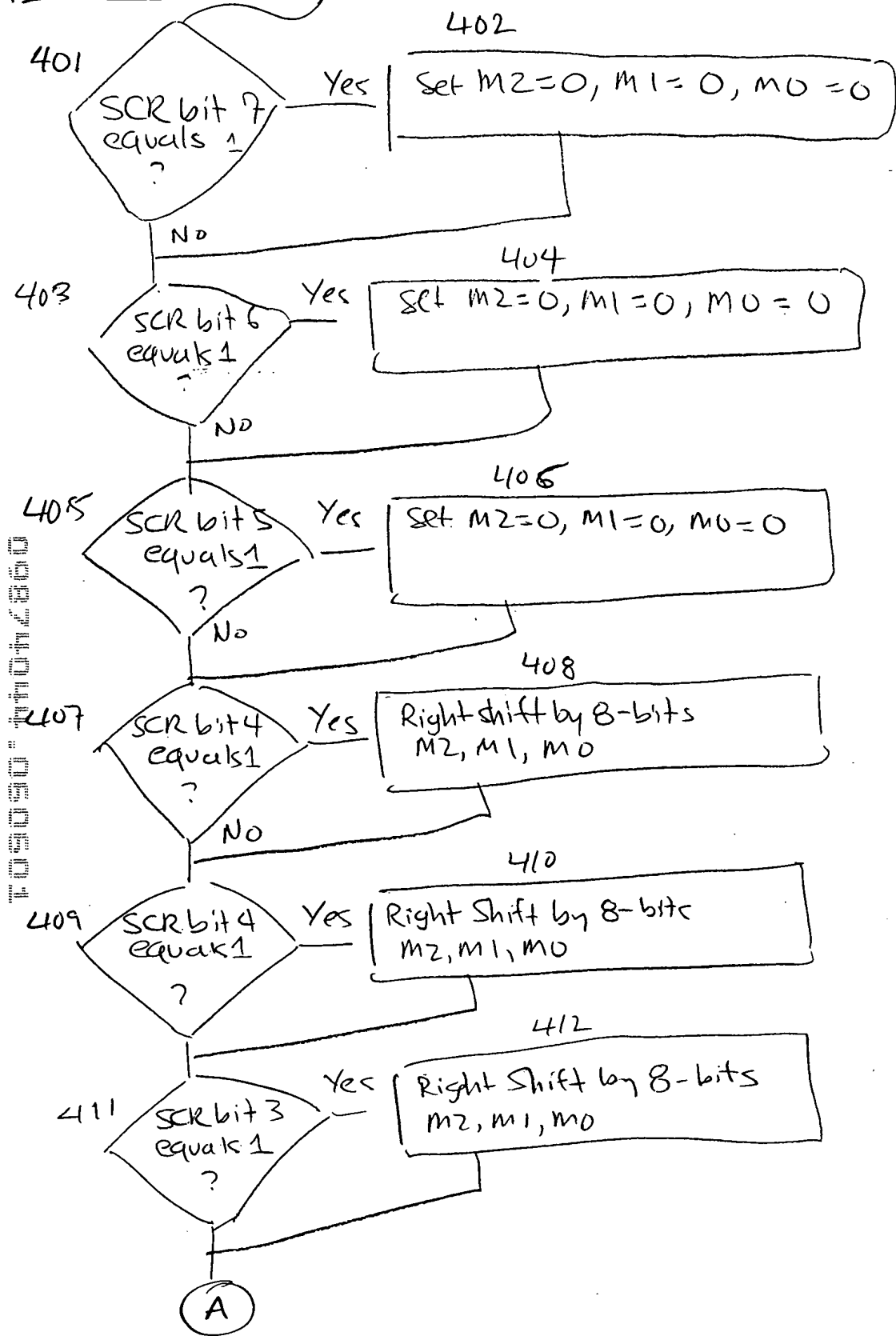


FIG 4B

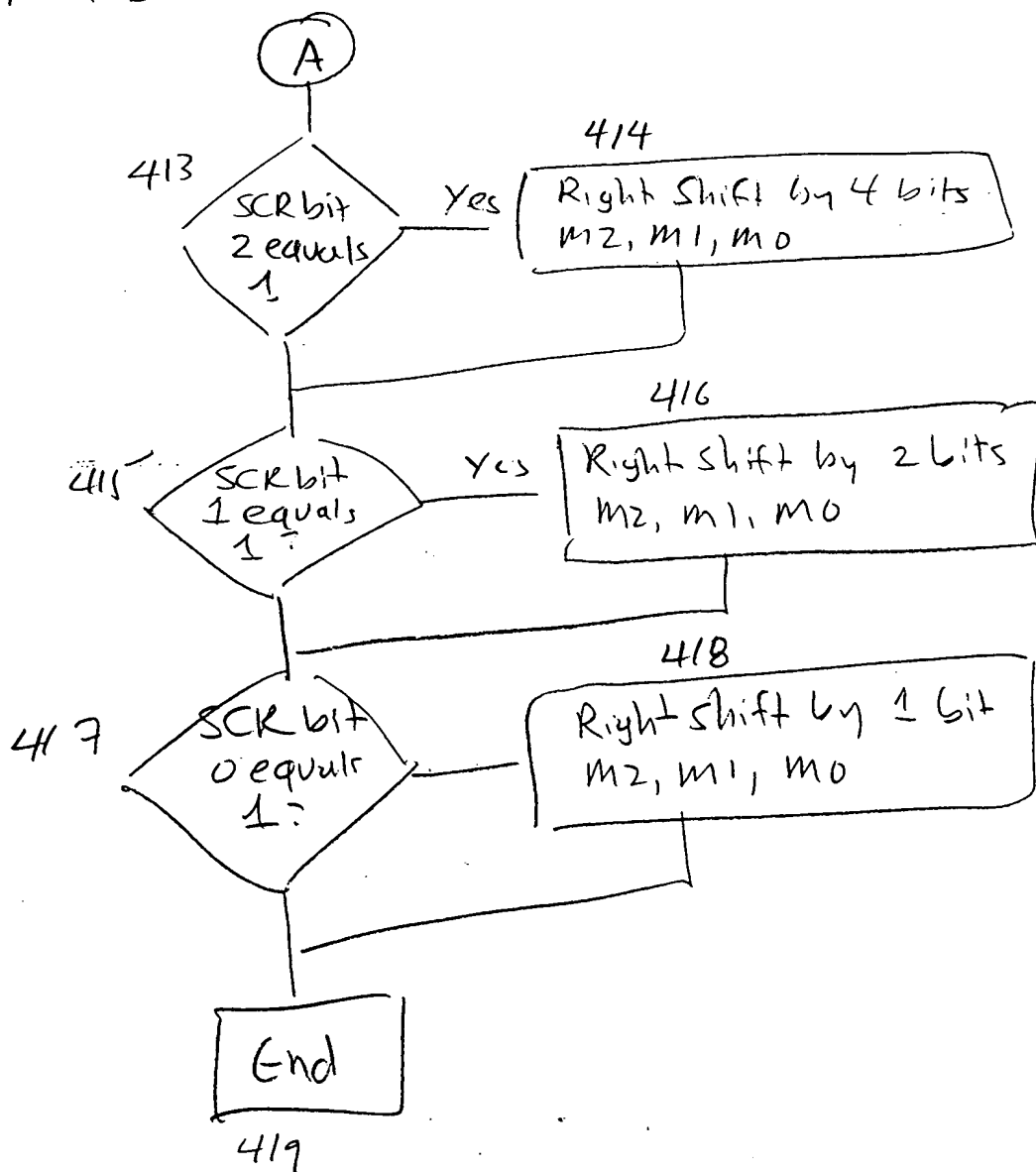


FIG 5

